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offset update switch

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 Gopal Gupta, Enrico Pontelli, Khayri A.M. Ali, Mats Carlsson, Manuel V. Hermenegildo  
 July 2001 **ACM Transactions on Programming Languages and Systems (TOPLAS)**,  
 Volume 23 Issue 4

Publisher: ACM Press

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Since the early days of logic programming, researchers in the field realized the potential for exploitation of parallelism present in the execution of logic programs. Their high-level nature, the presence of nondeterminism, and their referential transparency, among other characteristics, make logic programs interesting candidates for obtaining speedups through parallel execution. At the same time, the fact that the typical applications of logic programming frequently involve irregular computation ...

**Keywords:** Automatic parallelization, constraint programming, logic programming, parallelism, prolog

2 [RFC0891: DCN local-network protocols](#)

D. L. Mills

December 1983 rfc

Publisher: RFC Editor

Additional Information: [full citation](#)3 [Localization and timesynch: The flooding time synchronization protocol](#)

Miklós Maróti, Branislav Kusy, Gyula Simon, Ákos Lédeczi

 November 2004 **Proceedings of the 2nd international conference on Embedded networked sensor systems**

Publisher: ACM Press

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Wireless sensor network applications, similarly to other distributed systems, often require a scalable time synchronization service enabling data consistency and coordination. This paper describes the Flooding Time Synchronization Protocol (FTSP), especially tailored for applications requiring stringent precision on resource limited wireless platforms. The proposed time synchronization protocol uses low communication bandwidth and it is robust against node and link failures. The FTSP achieves ...



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IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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